



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
 United States Patent and Trademark Office  
 Address: COMMISSIONER FOR PATENTS  
 P.O. Box 1450  
 Alexandria, Virginia 22313-1450  
 www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 3956

SERIAL NUMBER 10/766,932	FILING DATE 01/30/2004  RULE	CLASS 156	GROUP ART UNIT 1763	ATTORNEY DOCKET NO. SEC.761D
-----------------------------	---------------------------------------	--------------	------------------------	------------------------------------

APPLICANTS

Ji-Soo Kim, Sunghnam-city, KOREA, REPUBLIC OF; ✓

Tae-Hyuk Ahn, Yongin-city, KOREA, REPUBLIC OF;  
 Won-Seok Lee, Sunghnam-city, KOREA, REPUBLIC OF; Wan-Jae Park, Suwon-city, KOREA, REPUBLIC OF;

\*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 09/695,068 10/25/2000 PAT 6,719,808 ✓

\*\* FOREIGN APPLICATIONS \*\*\*\*\*

REPUBLIC OF KOREA 99-46909 10/27/1999 ✓

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 06/14/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Examiner's Signature _____ Initials _____	STATE OR COUNTRY KOREA, REPUBLIC OF	SHEETS DRAWING 4 ✓	TOTAL CLAIMS 6 ✓	INDEPENDENT CLAIMS 1 ✓
--	---	---	--------------------------	------------------------	------------------------------

ADDRESS

20987  
 VOLENTINE FRANCO, & WHITT PLLC  
 ONE FREEDOM SQUARE  
 11951 FREEDOM DRIVE SUITE 1260  
 RESTON, VA  
 20190

TITLE

Method of and apparatus for manufacturing a semiconductor device using a polysilicon hard mask

☐ All Fees